TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

# 2SK3476

VHF- and UHF-band Amplifier Applications

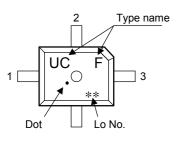
- Output power:  $P_O = 7.0 \text{ W} (\text{min})$
- Gain: G<sub>P</sub> = 11.4dB (min)
- Drain efficiency:  $\eta D = 60\%$  (min)

#### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V <sub>DSS</sub>	20	V
Gain-source voltage	V <sub>GSS</sub>	±5	V
Drain current	Ι <sub>D</sub>	3	А
Power dissipation	P <sub>D</sub> (Note 1)	20	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	-45~150	°C

Note 1: Tc = 25°C (When mounted on a 1.6 mm glass epoxy PCB)

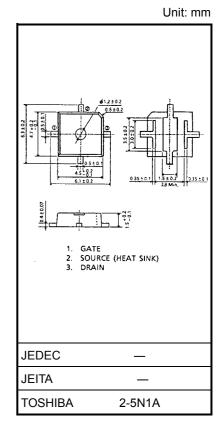
### Marking



Gate
Source (heat sink)
Drain

#### Caution

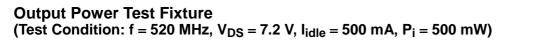
Please take care to avoid generating static electricity when handling this transistor.

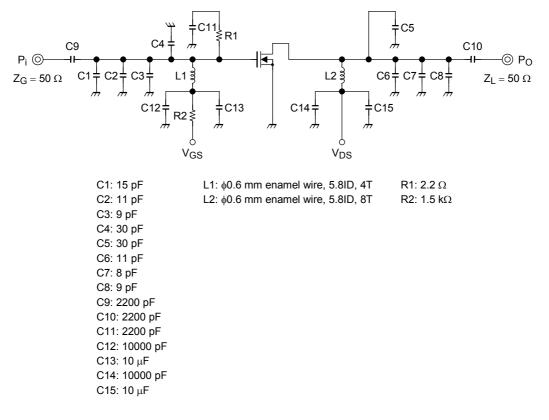


**Electrical Characteristics (Ta = 25°C)** 

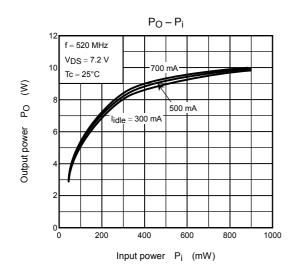
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain cut-off current	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			5	μA
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = 10 V			5	μA
Threshold voltage	V <sub>th</sub>	$V_{DS} = 7.2 \text{ V}, \text{ I}_{D} = 2 \text{ mA}$	0.55	1.05	1.55	V
Drain-source on-voltage	V <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 75 \text{ mA}$		18		mV
Forward transconductance	Y <sub>fs</sub>	$V_{DS} = 7.2 \text{ V}, \text{ I}_{DS} = 1 \text{ A}$	_	1		S
Input capacitance	C <sub>iss</sub>	$V_{DS} = 7.2 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		53		pF
Output capacitance	C <sub>oss</sub>	$V_{DS} = 7.2 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	49		pF
Output power	Po	V <sub>DS</sub> = 7.2 V,	7			W
Drain efficiency	η <sub>D</sub>	l <sub>idle</sub> = 500 mA (V <sub>GS</sub> = adjust),	60			%
Power gain	GP	f = 520 MHz, P <sub>i</sub> = 500 mW,	11.4			dB
Low voltage output power	P <sub>OL</sub>	V <sub>DS</sub> = 6.0 V, I <sub>idle</sub> = 500 mA (V <sub>GS</sub> = adjust), f = 520 MHz, P <sub>i</sub> = 500 mW,	5			W
Load mismatch	_	$\label{eq:VDS} \begin{array}{l} V_{DS} = 10 \ V, \ P_O = 7 \ W, \\ V_{GS} = adjust, \ P_i = adjust, \\ f = 520 \ MHz, \\ VSWR \ LOAD \ 20:1 \ all \ phase \end{array}$	No degradation			

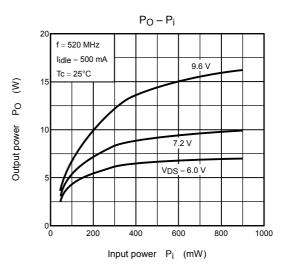
Note 1: These characteristic values are measured using measurement tools specified by Toshiba.

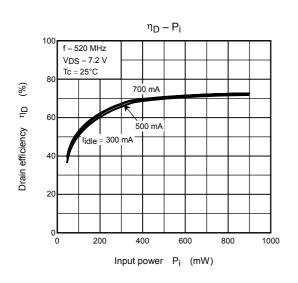


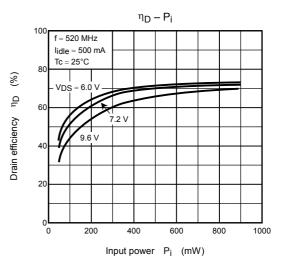


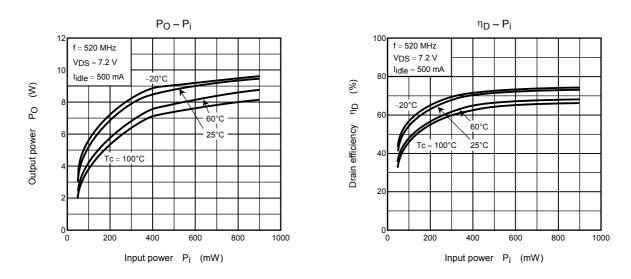
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Note 2: These are only typical curves and devices are not necessarily guaranteed at these curves.

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